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PTO/SB/21 (08-00)

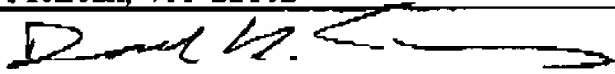
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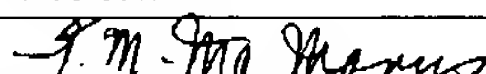
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TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i>	Application Number	09/707,844	
	Filing Date	November 8, 2000	
	First Named Inventor	Hidetoshi ISHIDA et al.	
	Group Art Unit	2814	
	Examiner Name	Dana FARAHANI	
Total Number of Pages in This Submission	8	Attorney Docket Number	740819-448

ENCLOSURES (check all that apply)		
<input type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Fee Attached <input checked="" type="checkbox"/> Amendment / Reply <input checked="" type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input type="checkbox"/> Information Disclosure Statement <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Response to Missing Parts/Incomplete Application <input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53	<input type="checkbox"/> Assignment Papers (for an Application) <input type="checkbox"/> Drawing(s) <input type="checkbox"/> Declaration and Power of Attorney <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition <input type="checkbox"/> Petition to Convert to a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD, Number of CD(s) _____	<input type="checkbox"/> After Allowance Communication to Group <input type="checkbox"/> Other <div style="text-align: center;"> FAX RECEIVED JAN 30 2003 TECHNOLOGY CENTER 2800 </div>
Remarks		<input checked="" type="checkbox"/> The Commissioner is hereby authorized to charge any additional fees required or credit any overpayments to Deposit Account No. 19-2380 for the above identified docket number.

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	Donald R. Studebaker, Reg. No. 32,815 Nixon Peabody LLP 8180 Greensboro Drive Suite 800 McLean, VA 22102
Signature	
Date	January 30, 2003

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Docket No. 740819-448

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Hidetoshi Ishida et al.

Serial No. 09/707,844

Filed: November 8, 2000

For: SEMICONDUCTOR DEVICE

) RESPONSE UNDER 37 CFR 1.116

) EXPEDITED PROCEDURE

) EXAMINING GROUP 2814

) Examiner: Dana Farahani

)

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AMENDMENT AFTER FINAL

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Commissioner for Patents
Washington, D.C. 20231

Sir:

In response to the final Office Action mailed November 4, 2002, please consider the following amendments and remarks in connection with the above-identified application.

IN THE CLAIMS:

Please amend claims 1 and 6 as follows:

1. (Twice Amended) A semiconductor device comprising:
- a semiconductor substrate;
- two semiconductor components provided on the principal surface of the surface; and
- multiple through holes, which pass from the principal surface through the backside of the substrate and are provided in a region of the substrate between the [at least] two components so as to substantially eliminate the electrical interference between the two semiconductor components; and
- a conductor film formed on the side faces of the through holes.

6. (Twice Amended) A semiconductor device comprising:

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